

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	69	703/13.ccls. and @pd>"20070801"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 13:44

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1090	(virtual adj machine) and monitor and translat\$5 and (operating adj system) and @ad<"20020101"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:16
L2	609	L1 and (simulat\$4 or emulat\$4)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:17
L3	414	L2 and (segment\$1 or descriptor\$1)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:50
L5	15	L3 and (overlap\$1 same translat\$4)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:56
L7	1	L3 and (substitute adj segment)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:56

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L1	1090	(virtual adj machine) and monitor and translat\$5 and (operating adj system) and @ad<"20020101"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:16
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L3	414	L2 and (segment\$1 or descriptor\$1)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2007/12/18 14:17


 A screenshot of the Google Scholar search interface. At the top, there's a navigation bar with links for "Web", "Images", "Video", "News", "Maps", and "more ». Below the navigation bar is a search bar containing the query "virtual machine" monitor translation. To the right of the search bar are date filters "1950" and "2001", a "Search" button, and a small "Ad" label with "Sc" and "Sc" below it.

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### [Analysis of the Intel Pentium's ability to support a secure virtual machine monitor - all 8 versions »](#)

JS Robin, CE Irvine - Proceedings of the 9th conference on USENIX Security ..., 2000 - portal.acm.org

... Examples include a protection primitive, address **translation** primitive, or a sub ...  
VMM requirements, it can still implement a hybrid **virtual machine monitor** (HVM ...

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### [System for recovery from a virtual machine monitor failure with a continuous guest dispatched to a ... - all 3 versions »](#)

T Inoue, H Umeno, S Tanaka, T Yamamoto, T Ohtsuki - US Patent 5,437,033, 1995 - Google Patents

... A **virtual machine monitor** or hyper- microprogram and a function to start a continuous ...  
(ii) In order to eliminate an address **translation** from 40 and if the ...

[Cited by 45 - Related Articles - Web Search](#)

### [Translation buffer for virtual machines with address space match - all 4 versions »](#)

AH Mason, JS Hall, PT Robinson, RT Witek - US Patent 5,319,760, 1994 - Google Patents

... context switching from one **virtual machine** to another, or to or from the **virtual machine monitor**, needlessly flushing entries in the **translation** buffer which ...

[Cited by 78 - Related Articles - Web Search](#)

### [VM/4: ACOS-4 virtual machine architecture](#)

S Nanba, N Ohno, H Kubo, H Morisue, T Ohshima, H ... - ACM SIGARCH Computer Architecture News, 1985 - portal.acm.org

... from the **virtual machine** and execute them with address **translation** from the ... it is possible to reduce the number of **virtual machine monitor** interventions and ...

[Cited by 37 - Related Articles - Web Search](#)

### [The Jalapeno virtual machine - all 12 versions »](#)

B Alpern, CR Attanasio, JJ Barton, MG Burke, P ... - IBM Systems Journal, 2000 - research.ibm.com

... The optimizing compiler's front end (**translation** to HIR ... the overall design of such a **virtual machine**. ... line measurement subsystem will **monitor** the performance ...

[Cited by 367 - Related Articles - Cached - Web Search](#)

### [Virtual machine monitors for scalable multiprocessors - all 3 versions »](#)

E Bugnion, SW Devine, M Rosenblum - US Patent 6,075,938, 2000 - Google Patents

... to run efficiently on large-scale shared memory multiprocessors without a large implementation effort is solved by a unique type of **virtual machine monitor**. ...

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### [Protection ring extension for computers having distinct virtual machine monitor and virtual machine ... - all 3 versions »](#)

PT Robinson, AH Mason, JS Hall - US Patent 5,522,075, 1996 - Google Patents

... 1.6 Virtual Machines and the **Virtual Machine Monitor** ... en enclosed by the circle (or


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### [Analysis of the Intel Pentium's ability to support a secure virtual machine monitor - all 8 versions »](#)

JS Robin, CE Irvine - Proceedings of the 9th conference on USENIX Security ..., 2000 - portal.acm.org

... can still implement a hybrid **virtual machine monitor** (HVM ... tecture system or an address

**translation system** to ... that specifies the privilege level of the **segment**. ...

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### [VM/4: ACOS-4 virtual machine architecture](#)

S Nanba, N Ohno, H Kubo, H Morisue, T Ohshima, H ... - ACM SIGARCH Computer Architecture News, 1985 - portal.acm.org

... which is the VM/k **virtual machine monitor**, is notified ... the physical address within the **virtual machine** is used ... machine, using the address **translation** table for ...

[Cited by 37 - Related Articles - Web Search](#)

### [Translation buffer for virtual machines with address space match - all 4 versions »](#)

AH Mason, JS Hall, PT Robinson, RT Witek - US Patent 5,319,760, 1994 - Google Patents  
... context switching from one **virtual machine** to another, or to or from the **virtual machine monitor**, needlessly flushing entries in the **translation** buffer which ...

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### [Memory management and protection system for virtual memory in computer system - all 2 versions »](#)

H Segawa, S Sungho, T Okamoto, S Asano, K Maeda, M ... - US Patent 5,890,189, 1999 - patentstorm.us

... 5426750 **Translation** lookaside buffer apparatus and method with ... for computers having distinct **virtual machine monitor** and **virtual machine** address spaces ...

[Cited by 22 - Related Articles - Cached - Web Search](#)

### [Capability-Based Addressing - all 6 versions »](#)

RS Fabry - Communications, 1974 - portal.acm.org

... A hardware address **translation** scheme which never requires the modification of the ... scheme uses a main memory hash table for obtaining a **segment's** location in ...

[Cited by 257 - Related Articles - Web Search](#)

### [Virtual machine system with address \*\*translation\*\* buffer for holding host and plural guest entries - all 3 versions »](#)

M Watanabe, S Abe - US Patent 4,816,991, 1989 - Google Patents

... 45 **monitor**) is utilized as the guest VM identifying infor ... 2. A **virtual machine** system according to claim 1 ... each entry of said address **translation** buffer further ...

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### [Virtual machine monitors for scalable multiprocessors - all 3 versions »](#)

E Bugnion, SW Devine, M Rosenblum - US Patent 6,075,938, 2000 - Google Patents

... with the additional support for **segment** drivers that ... must be intercepted and remapped


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Doran, R.W.;  
[Computer](#)  
Volume 21, [Issue 10](#), Oct. 1988 Page(s):20 - 28  
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